

**RESPONSE UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2822**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jack A. Mandelman, et al.

Examiner: Monica Lewis

Serial No: 10/812,283

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For: METHOD FOR FORMING DUAL
WORK-FUNCTION HIGH-PERFORMANCE
SUPPORT MOSFETs IN EDRAM ARRAYS

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Commissioner for Patents
United States Patent and Trademark Office
Alexandria, VA 22313-1450

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Sir:

In response to the Final Office Action dated September 8, 2008, Applicants respectfully request that the following remarks be entered as a matter of record in the above-identified patent application.

CERTIFICATE OF ELECTRONIC FILING

I hereby certify that this correspondence is being deposited with the United States Patent & Trademark Office via Electronic Filing through the United States Patent and Trademark Office e-business website, on the date shown below.

Dated: December 4, 2008



Leslie S. Szivos